KMB080N75PA

N CHANNEL MOS FIELD EFFECT TRANSISTOR

General Description

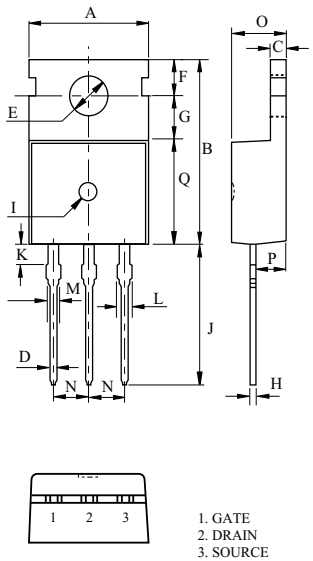
It s mainly suitable for low voltage applications such as automotive, DC/DC converters and a load switch in battery powered applications

FEATURES

· V_{DSS}= 75V, I_D= 80A

· Drain-Source ON Resistance :

$R_{DS(ON)}$=12m Ω (Max.) @V_{GS} = 10V



DIM	MILLIMETERS
A	9.9 ± 0.2
B	15.95 MAX
C	1.3+0.1/-0.05
D	0.8 ± 0.1
E	ø3.6 ± 0.2
F	2.8 ± 0.1
G	3.7
H	0.5+0.1/-0.05
I	ø1.5
J	13.08 ± 0.3
K	1.46
L	1.4 ± 0.1
M	1.27 ± 0.1
N	2.54 ± 0.2
O	4.5 ± 0.2
P	2.4 ± 0.2
Q	9.2 ± 0.2

TO-220AB

MOSFET MAXIMUM RATING (Ta=25℃ Unless otherwise noted)

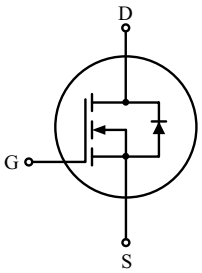
CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V_{DSS}	75	V
Gate-Source Voltage		V_{GSS}	±25	V
Drain Current	DC	I_D*	80	A
	Pulsed (Note 1)	I_{DP}	320	A
Drain-Source Diode Forward Current		I_S	80	A
Drain Power Dissipation		P_D* (25℃)	300	W
Maximum Junction Temperature		T_j	-55～175	℃
Storage Temperature Range		T_{stg}	-55～175	℃

Note1) Pulse Test : Pulse width ≤ 10μS Duty cycle ≤1%

Thermal Characteristics

CHARACTERISTIC	SYMBOL	RATING	UNIT
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5	℃/W
Thermal Resistance, Junction-to-Case	R_{thJC}	0.5	℃/W

Equivalent Circuit



MOSFET Electrical Characteristics (Ta=25℃ Unless otherwise noted)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	I_D=250㎂, V_{GS}=0V	75	-	-	V
Drain Cut-off Current	I_{DSS}	V_{DS}=75V, V_{GS}=0V,	-	-	10	㎂
Gate Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
Gate Threshold Voltage	V_{th}	V_{DS}=V_{GS}, I_D=250㎂	2	-	4	V
Drain-Source ON Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=40A	-	10	12	mΩ
Forward Transconductance	g_{FS}	V_{DS}=15V, I_D=40A	-	20	-	S
Dynamic						
Input Capacitance	C_{iss}	V_{DS}=25V, V_{GS}=0V, f=1.0MHz	-	3700	-	pF
Output Capacitance	C_{oss}		-	730	-	
Reverse Transfer Capacitance	C_{rss}		-	240	-	
Total Gate Charge	Q_g	V_{DS}= 60V, V_{GS}= 10V, I_D=40A (Note1,2)	-	117	-	nC
Gate-Source Charge	Q_{gs}		-	27	-	
Gate-Drain Charge	Q_{gd}		-	47	-	
Turn-On Delay Time	$t_{d(on)}$	V_{DD}= 30.5V, I_D=40A, R_G=25 Ω (Note1,2)	-	25	-	ns
Turn-On Rise Time	t_r		-	25	-	
Turn-Off Delay Time	$t_{d(off)}$		-	66	-	
Turn-Off Fall Time	t_f		-	30	-	

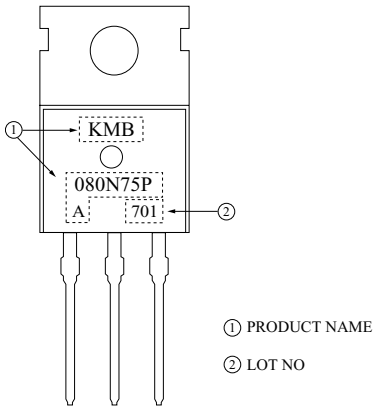
Note 1) Pulse Test : Pulse width ≤10㎲, Duty Cycle ≤1%.

Note 2) Essentially Independent of Operating Temperature.

DIODE Electrical Characteristics (Ta=25℃ Unless otherwise noted)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Diode Forward Voltage	V_{SD}	I_{SD}=80A, V_{GS}=0V	-	-	1.5	V
Reverse Recovery Time	T_{rr}	V_{GS}=0V, I_S=80A, dIF/dt=100A/㎲	-	132	-	ns

Marking



① PRODUCT NAME

② LOT NO

Fig 1. I_D - V_{DS}

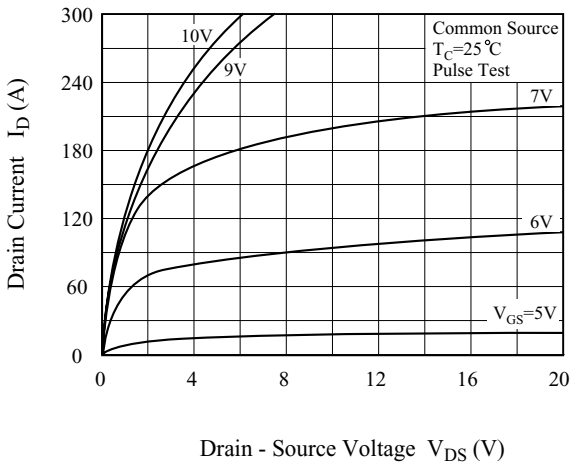


Fig 2. $R_{DS(ON)}$ - I_D

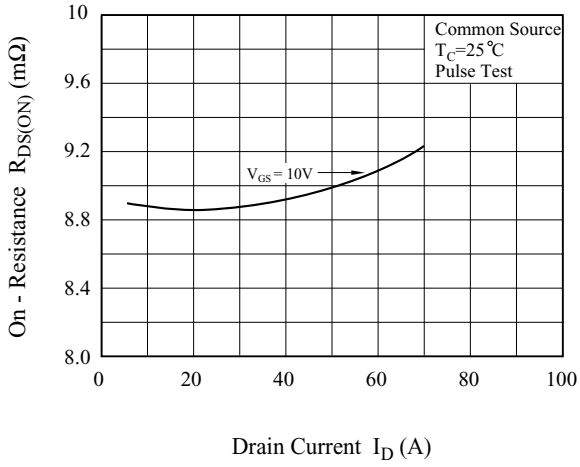


Fig 3. I_D - V_{GS}

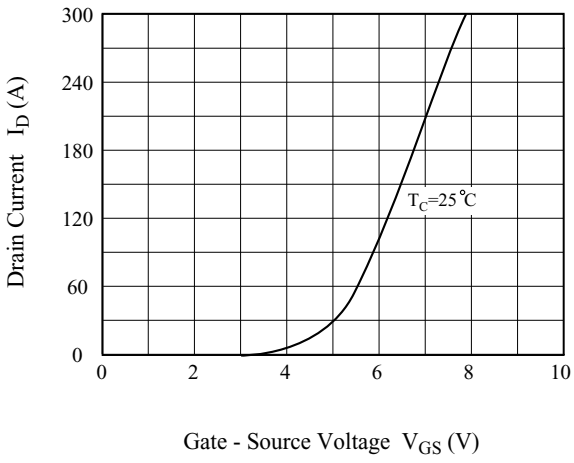


Fig 4. $R_{DS(ON)}$ - T_j

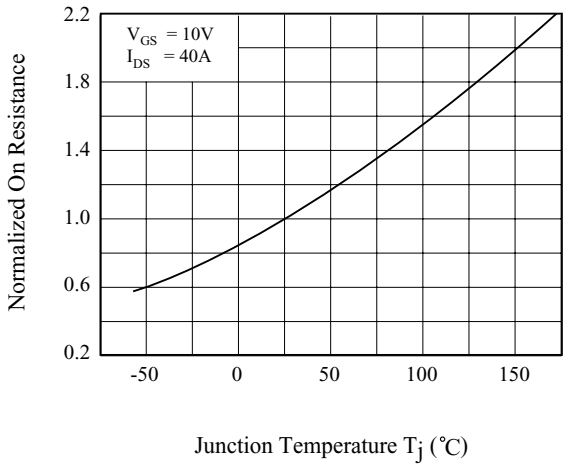


Fig 5. V_{th} - T_j

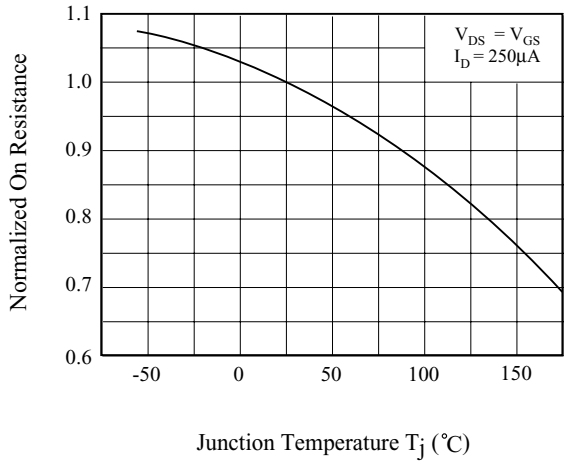


Fig 6. I_{DR} - V_{DSF}

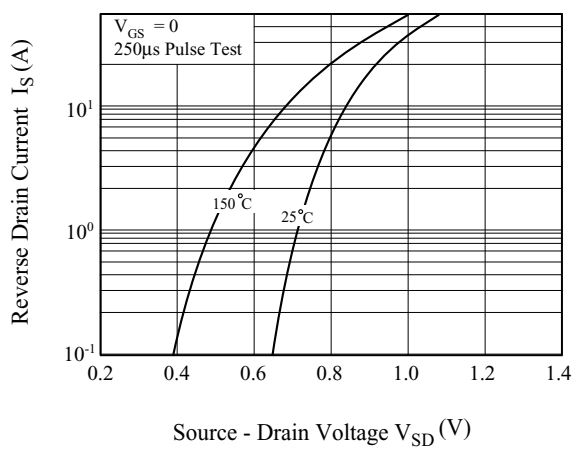


Fig 7. Q_g - V_{GS}

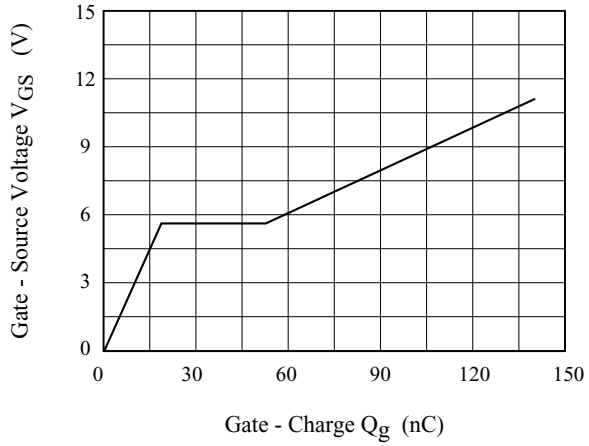


Fig 8. C - V_{DS}

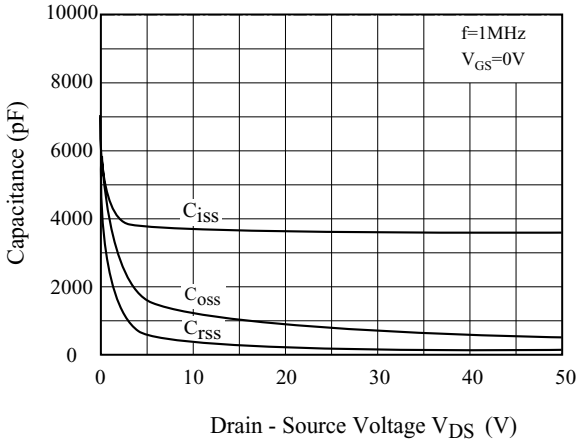


Fig 9. Safe Operation Area

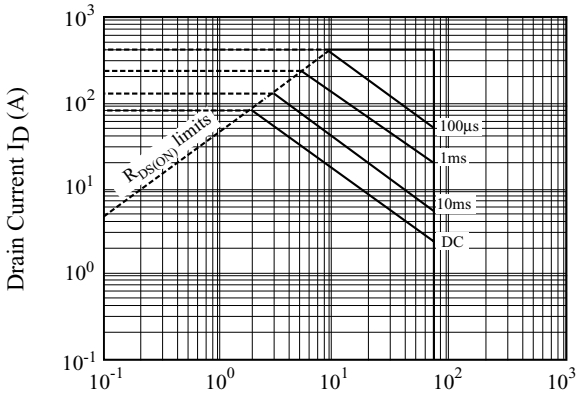
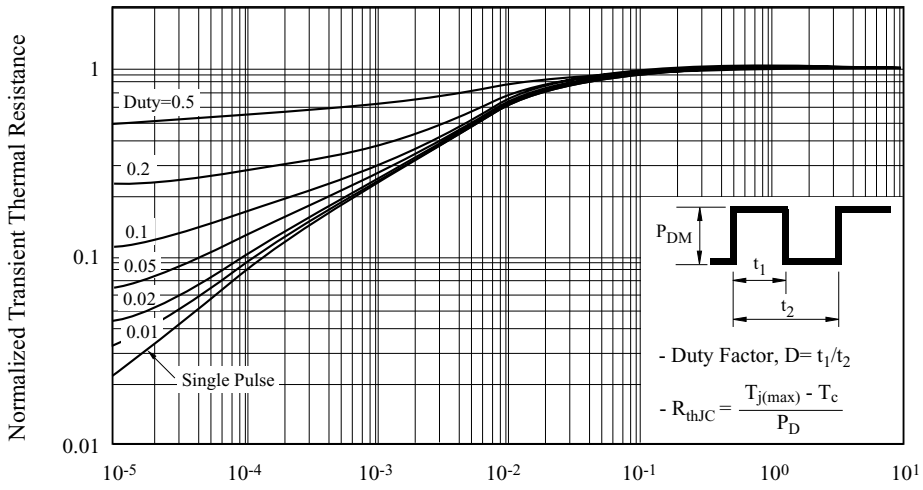
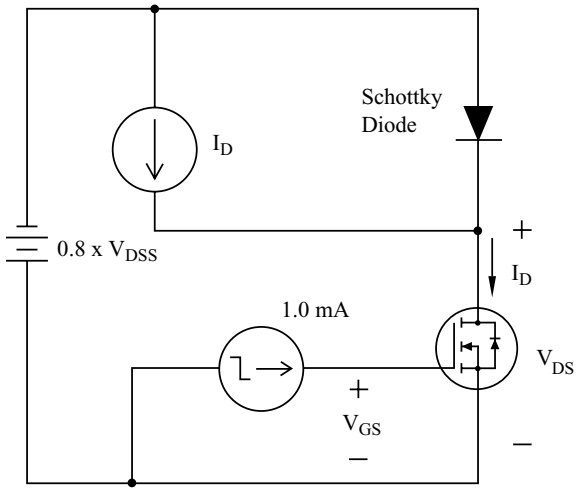
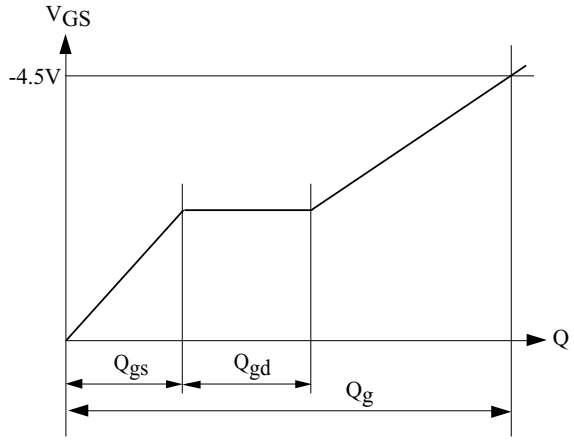


Fig 10. R_{th}

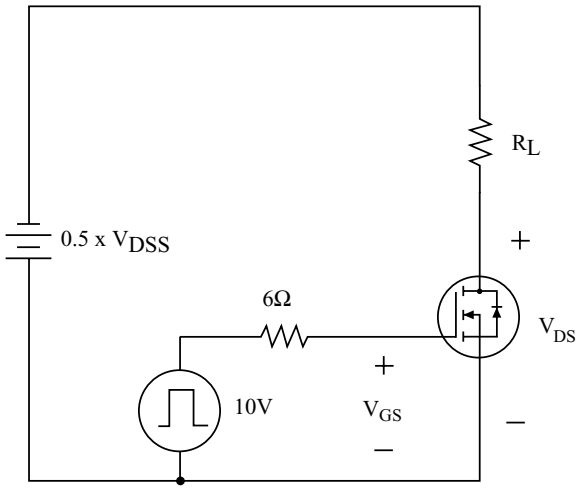


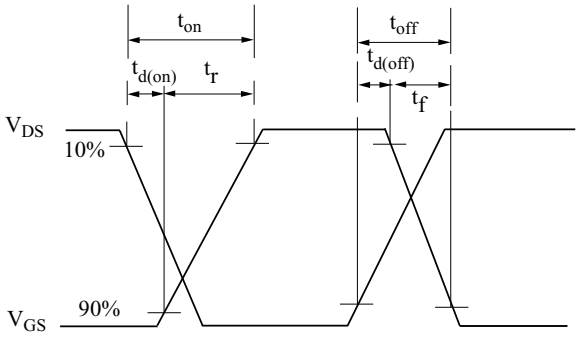
- Gate Charge



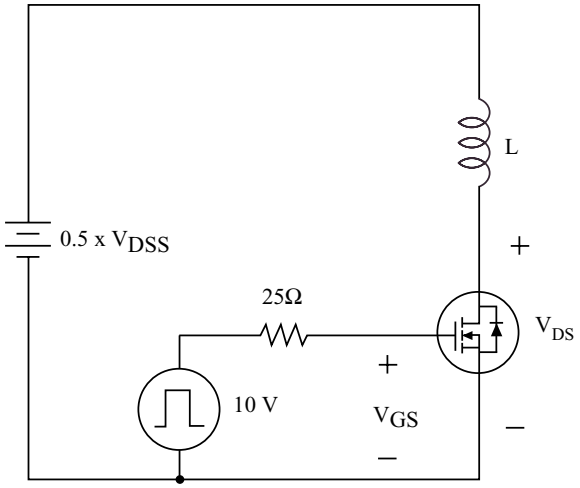


- Resistive Load Switching

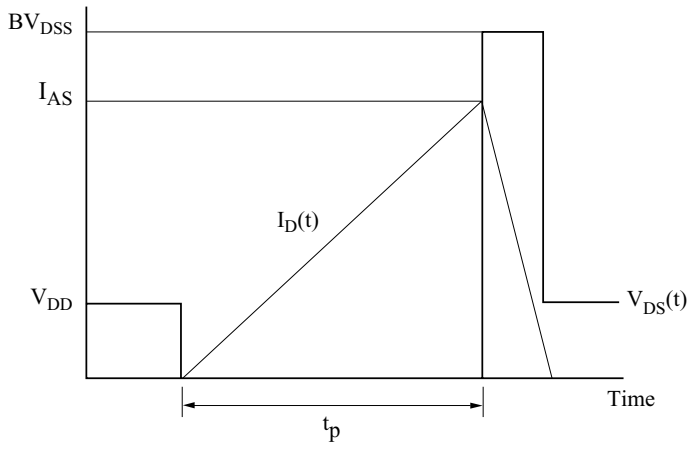




- Single Pulsed Avalanche Energy



$$E_{AS} = \frac{1}{2} LI_{AS}^2 \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$



- Source - Drain Diode Reverse Recovery and dv /dt